

AMENDMENTS TO THE CLAIMS

Please **AMEND** claims 11-15 as shown below.

The following is a complete list of all claims in this application.

1-10. (Cancelled)

11. (Currently Amended) A thin film transistor array substrate for a liquid crystal display, comprising:

- an insulating substrate;
- a gate line formed on the substrate;
- a ~~common-electrode~~ storage capacitor line proceeding parallel to the gate line;
- a storage capacitor electrode connected to the ~~common-electrode~~ storage capacitor line;
- a gate insulating layer formed on the gate line, the ~~common-electrode~~ storage capacitor line, and the storage capacitor electrode;
- a data line formed on the gate insulating layer;
- a pixel region defined by the gate line and the data line;
- a protective layer formed on the data line; and
- a pixel electrode formed on the protective layer with opening patterns,

wherein the storage capacitor electrode is extended along an edge of the pixel region, and

the pixel electrode entirely covers ~~entire width of a portion of~~ the storage capacitor electrodes electrode at particular regions.

12. (Currently Amended) The thin film transistor array substrate of claim 11, wherein the storage capacitor electrode is ~~respectively provided~~ formed at a left side and a right side of ~~each the pixel region electrode~~.

13. (Currently Amended) The thin film transistor array substrate of claim 11, wherein the ~~common-electrode~~ storage capacitor line comprises two ~~separate~~ lines in the pixel region.

14. (Currently Amended) The thin film transistor array substrate of claim 11, wherein each opening pattern comprises:

a horizontal opening portion formed at ~~the boundary~~ a portion of the pixel electrode dividing ~~bisecting~~ the pixel electrode into an upper region and a lower region; and

inclined opening portions formed at the upper region and the lower region of the pixel electrode while proceeding perpendicular to each other.

15. (Currently Amended) A thin film transistor array substrate, comprising:
an insulating substrate;
a pixel region formed on the insulating substrate;
a common-electrode storage capacitor line having a main portion and a branch portion
extended from the main portion and with branched electrodes, said common-electrode line being
formed on the insulating substrate, the branch portion extended along an edge of the pixel region;
and

a pixel electrode insulated from the ~~common-electrode~~ storage capacitor line and having a first opening pattern ~~patterns~~,

wherein the pixel ~~electrodes cover~~ electrode entirely covers a portion of the branch
portion ~~entire width of the branched electrodes of the common-electrode lines at particular~~
~~regions when viewed from a top side.~~